

10/715398

METHOD OF SETTING A SYSTEM TIME CLOCK AT THE START OF AN MPEG SEQUENCE

Publication number: JP2003520514T

Publication date: 2003-07-02

Inventor:

Applicant:

Classification:

- international: **H04N5/92; G11B27/00; G11B27/034; G11B27/10; G11B27/28; G11B27/30; H04J3/00; H04N5/00; H04N7/24; H04N7/62; H04N9/804; H04N5/783; H04N5/85; H04N5/92; G11B27/00; G11B27/031; G11B27/10; G11B27/28; G11B27/30; H04J3/00; H04N5/00; H04N7/24; H04N7/52; H04N9/804; H04N5/783; H04N5/84; (IPC1-7): H04J3/00; H04N5/92**

- European: G11B27/00V; G11B27/034; G11B27/10A1; G11B27/28; G11B27/30C; H04N5/00M10; H04N7/24C10; H04N7/62; H04N9/804B

Application number: JP20010552647T 20010105

Priority number(s): EP20000200038 20000110; WO2001EP00110 20010105

Also published as:



WO0152554 (A1)
EP1163802 (A1)
US2003058948 (A1)
EP1163802 (A0)
CN1606357 (A)

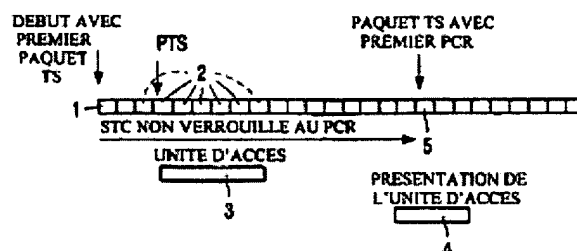
more >>

Report a data error here

Abstract not available for JP2003520514T

Abstract of corresponding document: **WO0152554**

Method to enable a local system time clock counter (STC) of a receiving recording device to lock to program clock reference (PCR) information comprised in a received real time sequence of information signal packets, such as MPEG2 Transport Stream packets. The method comprising determining the number of cycles between arrival of the first information signal packet and the arrival of the information signal packet comprising the first Program Clock Reference (PCR) information. This information is stored as an attribute of the stored sequence.



Data supplied from the esp@cenet database - Worldwide